Catalog description:
This course examines the basic principles of computer systems and how these concepts relate to the design of such systems. Both hardware and software concepts are considered and the interdependence between them are covered. Determining basic tradeoffs and related decisions are covered. Logic level designs, data representations, computer circuits, fundamental computer operations, program creation, I/O programming, processing elements, links and interfaces, memory hierarchy, and memory management are covered. Three lecture hours per week.

Prerequisites: CSC 115 or CSC 202J, and CSC 105 or CSC 215, and PHS 205.

Goals:
The goals of this course are to introduce students to the concepts of the organization and architecture of computer systems, from the physical and logic levels through the intermediate levels to the higher-level-language level, and the methodologies and problem-solving strategies used define and implement the necessary ingredients. Specific goals are to:

CG01: present the concept of a computer system as a series of levels, each with its own properties and methodologies;
CG02: introduce a series of problem solving methodologies relating to the various system levels;
CG03: discuss problem-solving techniques based on the presented methodologies.

Objectives:
Upon successful completion of the course, a student will have:

CO01: demonstrated knowledge of the concepts of computer architecture and organization;
CO02: demonstrated the ability to apply appropriate problem-solving strategies to solve a selection of typical problems in computer architecture and organization;
CO03: gained hands-on experience designing an ALU based on a set of specified requirements;
CO04: implemented a simple instruction set computer with a control unit and a data path;
CO05: developed a good understanding of memory hierarchy in a computer system;
CO06: developed an understanding of an I/O subsystem supporting processor programmed I/O, direct memory access and interrupt structures;
CO07: developed an understanding of basic concepts of a multi-core processor design;
CO08: participated in at least one group project that involves solution design, analysis and evaluation.

Program Outcome vs. Course Objectives matrix

<table>
<thead>
<tr>
<th>Program Objective</th>
<th>CO01</th>
<th>CO02</th>
<th>CO03</th>
<th>CO04</th>
<th>CO05</th>
<th>CO06</th>
<th>CO07</th>
<th>CO08</th>
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</thead>
<tbody>
<tr>
<td>PO-A: apply knowledge of computing and math</td>
<td>✓</td>
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<td>PO-B: analyze a problem and define its computing requirements</td>
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<td>✓</td>
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<td>PO-C: design, implement and evaluate applications</td>
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<td>PO-D: function effectively in teams to accomplish a common goal</td>
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<td>PO-E: professional, ethical, and social responsibilities</td>
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</table>

Section  | Time         | Room  | Final Exam
---      | ---          | ---   | ---
nn      | days and times | location | date and time

Office Hours: days and times
<table>
<thead>
<tr>
<th>Program Objective (condensed form)</th>
<th>CO01</th>
<th>CO02</th>
<th>CO03</th>
<th>CO04</th>
<th>CO05</th>
<th>CO06</th>
<th>CO07</th>
<th>CO08</th>
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<tbody>
<tr>
<td><strong>PO-F:</strong> communicate effectively with a range of audiences</td>
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<td><strong>PO-G:</strong> local and global impact of computing on people and society</td>
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<td><strong>PO-H:</strong> need for continuing professional development</td>
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<td>✓</td>
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<tr>
<td><strong>PO-I:</strong> use current techniques, skills, and tools</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>✓</td>
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<tr>
<td><strong>PO-J:</strong> apply theory and principles to model and design systems</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>✓</td>
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<tr>
<td><strong>PO-K:</strong> apply design and development principles in constructing software</td>
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</table>

Note: Full statements of the Program Outcomes (program objectives) for the Computer Science Major can be found in the document *Computer Science Major Program Educational Objectives and Program Outcomes* on the Assessment page of the Computer Science Major (cs.salemstate.edu).

**Topics:**

- **Computers and their applications**
  - hardware and software
- **Data representations**
  - text
  - numeric data types
    - non-negative integers
    - signed integers
    - range and scaling
    - real numbers
  - evaluation of expressions
- **Logic level design**
  - elementary logic gates
  - combinational logic design
  - elementary sequential circuits
- **Computer circuits**
  - electrical properties
  - combinational logic implementations
  - important combinational circuits
  - sequential circuits
  - link connections
  - integrated circuits and technologies
- **Fundamental computer operations**
  - machine language and assembler language instructions
  - stacks
  - procedures
  - macros
  - instruction execution time
- **Program creation**
  - assemblers
  - compilers
  - linking and address adjustment
  - loading and address adjustment
- **Input/output programming**
  - programmed I/O
  - interrupt I/O
  - direct memory access
  - I/O elements
- **Processing elements**
  - macroinstruction execution
  - internal bus transfers

**Note:**

- PL2(1)
- AR2(4)
- AR1(3)
- AR1(4)
- AR3(3), AR6(2)
- AR5(1.5)
- AR3(8), AR6(3)
- detailed internal architecture example
- microcontrol
- reduced instruction set computers (RISC)
- packaging

- Links and interfaces
  - system buses
  - interfaces
  - data links

- Memory hierarchy
  - mass storage
  - main memory
  - multiple-port memory
  - cache memory
  - hierarchy design

- Memory management
  - mass storage management
  - main memory management
  - memory management hardware
  - virtual memory

- Operating systems
  - uniprogramming systems
  - multiprogramming systems
  - organization of a multiprogramming system
  - sharing resources

- Parallel processing
  - multiprocessing
  - pipelining
  - vector and matrix processing
  - high-performance computing

AR5(1.5)  
AR4(2.5)  
AR4(2.5)  
AR6(2)  
AR7(3)

This course provides an implementation-independent treatment of the subject, emphasizing general and widely applicable principles rather than focusing on implementation methods, which may be specific to a particular type or model of computer. To give students hands-on experiences, laboratory activities are used in the course. Among these laboratory activities, at least 2 require students to work in teams to accomplish larger-scale assignments.

The course grade will be determined using the following approximate weights: final examination - 25%, midterm examination - 25%, laboratory activities - 25%, other tests, quizzes, and written homework - 25%.

### Course Objective / Assessment Mechanism matrix

<table>
<thead>
<tr>
<th></th>
<th>Homework</th>
<th>Exams</th>
<th>Laboratory Activities</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CO01</strong></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td><strong>CO02</strong></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
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<td><strong>CO03</strong></td>
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<td>✔</td>
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<td><strong>CO08</strong></td>
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Bibliography:


Academic Integrity Statement:

“Salem State University assumes that all students come to the University with serious educational intent and expects them to be mature, responsible individuals who will exhibit high standards of honesty and personal conduct in their academic life. All forms of academic dishonesty are considered to be serious offences against the University community. The University will apply sanctions when student conduct interferes with the University primary responsibility of ensuring its educational objectives.” Consult the University catalog for further details on Academic Integrity Regulations and, in particular, the University definition of academic dishonesty.

The Academic Integrity Policy and Regulations can be found in the University Catalog and on the University website (http://catalog.salemstate.edu/content.php?catoid=13&navoid=1295#Academic_Integrity). The formal regulations are extensive and detailed - familiarize yourself with them if you have not previously done so. A concise summary of and direct quote from the regulations: "Materials (written or otherwise) submitted to fulfill academic requirements must represent a student's own efforts". Submission of other's work as one's own without proper attribution is in direct violation of the University's Policy and will be dealt with according to the University's formal Procedures. Copying without attribution is considered cheating in an academic environment - simply put, do not do it!

University-Declared Critical Emergency Statement:

In the event of a university-declared emergency, Salem State University reserves the right to alter this course plan. Students should refer to www.salemstate.edu for further information and updates. The course attendance policy stays in effect until there is a university-declared critical emergency.

In the event of an emergency, please refer to the alternative educational plans for this course, which will be distributed via standing class communication protocols. Students should review the plans and act accordingly. Any required material that may be necessary will have been previously distributed to students electronically or will be made available as needed via email and/or Internet access.

Equal Access Statement:

"Salem State University is committed to providing equal access to the educational experience for all students in compliance with Section 504 of The Rehabilitation Act and The Americans with Disabilities Act and to providing all reasonable academic accommodations, aids and adjustments. Any student who has a documented disability requiring an accommodation, aid or adjustment should speak with the instructor immediately. Students with Disabilities who have not previously done so should provide documentation to and schedule an appointment with the Office for Students with Disabilities and obtain appropriate services."
Note: This syllabus represents the intended structure of the course for the semester. If changes are necessary, students will be notified in writing and via email.